IN THE CLAIMS

1-15 (Canceled)

16. (New) An apparatus for making a correspondence between digital information in electric signal form and information in magnetic signal form recorded in helical tracks on a magnetic recording tape, comprising:

a magnetic transducing head;

a head driving mechanism which causes said magnetic transducing head to scan on said helical tracks from a preamble portion disposed at a lower side of said magnetic recording tape preceding a digital signal information portion that includes a plurality of data blocks; and

a control circuit which determines a start timing of said digital information in response to a plurality of spaced-apart header signals in said preamble portion transduced between said head and said tape,

wherein each of said header signals includes a synchronizing signal, and an address signal.

17. (New) The apparatus of claim 16, wherein each of said header signals further includes a parity signal for correcting an error.

18. (New) The apparatus of claim 17, wherein each of said header signals further includes an identification signal for controlling said digital information.

19. (New) The apparatus of claim 16, wherein said control circuit further determines and protects a synchronous state based on said header signals already transduced.

20. (New) An apparatus for recording digital information into helical tracks on a magnetic tape, comprising:

a rotary magnetic head;

a head driving mechanism which causes said rotary magnetic head to scan on said helical tracks from a preamble portion disposed at a lower side of said magnetic recording tape preceding a digital signal information portion that includes a plurality of data blocks; and

a control circuit which controls a recording sequence to record header signals spaced apart in said

preamble portion before recording said digital information on each of said helical tracks,

wherein each of said header signals includes a synchronizing signal, and an address signal, and

wherein a timing for start of reproducing of said digital information is determined in response to reading said synchronizing signals while reproducing.

21. (New) The apparatus of claim 20, wherein each of said header signals further includes a parity signal for correcting an error.

22. (New) The apparatus of claim 21, wherein each of said header signals further includes an identification signal for controlling said digital information.

23. (New) The apparatus of claim 21, wherein said control circuit records said header signals for protecting a synchronous state in reproducing mode.